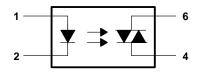
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- 250 V Phototriac Driver Output
- Gallium-Arsenide-Diode Infrared Source and Optically-Coupled Silicon Traic Driver (Bilateral Switch)
- UL Recognized ... File Number E65085
- High Isolation ... 7500 V Peak
- Output Driver Designed for 220 V ac
- Standard 6-Terminal Plastic DIP
- Directly Interchangeable with Motorola MOC3020, MOC3021, MOC3022, and MOC3023
- Direct Replacements for:
  - TRW Optron OPI3020, OPI3021, OPI3022, and OPI3023;
  - General Instrument MCP3020, MCP3021, and MCP3022;
  - General Electric GE3020, GE3021, GE3022, and GE3023

# MOC3020 - MOC3023 ... PACKAGE (TOP VIEW) ANODE [ 1 6 ] MAIN TERM CATHODE [ 2 5 ] TRIAC SUB† NC [ 3 4 ] MAIN TERM

† Do not connect this terminal NC – No internal connection

## logic diagram



# absolute maximum ratings at 25°C free-air temperature (unless otherwise noted)†

Input-to-output peak voltage, 5 s maximum duration, 60 Hz (see Note 1)	7.5 kV
Input diode reverse voltage	3 V
Input diode forward current, continuous	50 mA
Output repetitive peak off-state voltage	400 V
Output on-state current, total rms value (50-60 Hz, full sine wave): T <sub>A</sub> = 25°C	100 mA
$T_A = 70^{\circ}C$	50 mA
Output driver nonrepetitive peak on-state current (t <sub>w</sub> = 10 ms, duty cycle = 10%, see Figu	ure 7) 1.2 A
Continuous power dissipation at (or below) 25°C free-air temperature:	
Infrared-emitting diode (see Note 2)	100 mW
Phototriac (see Note 3)	300 mW
Total device (see Note 4)	330 mW
Operating junction temperature range, T <sub>J</sub>	40°C to 100°C
Storage temperature range, T <sub>stq</sub>	
Lead temperature 1.6 (1/16 inch) from case for 10 seconds	

<sup>†</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

- NOTES: 1. Input-to-output peak voltage is the internal device dielectric breakdown rating.
  - 2. Derate linearly to 100°C free-air temperature at the rate of 1.33 mW/°C.
  - 3. Derate linearly to 100°C free-air temperature at the rate of 4 mW/°C.
  - 4. Derate linearly to 100°C free-air temperature at the rate of 4.4 mW/°C.



# **MOC3020 THRU MOC3023** OPTOCOUPLERS/OPTOISOLATORS

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## electrical characteristics at 25°C free-air temperature (unless otherwise noted)

PARAMETER		TEST CONDITIONS		MIN	TYP	MAX	UNIT	
$I_{R}$	Static reverse current		V <sub>R</sub> = 3 V			0.05	100	μΑ
٧F	Static forward voltage		I <sub>F</sub> = 10 mA			1.2	1.5	V
I <sub>(DRM)</sub>	Repetitive off-state current, either direction		$V_{(DRM)} = 400 V,$	See Note 5		10	100	nA
dv/dt	Critical rate of rise of off-state voltage		See Figure 1			100		V/μs
dv/dt(c)	Critical rate of rise of con	nmutating voltage	voltage I <sub>O</sub> = 15 mA, See Figure 1			0.15		V/μs
ŀFТ	Input trigger current, either direction	MOC3020			15	30	mA	
		MOC3021	Output oupply voltogo 3 V			8		15
		MOC3022	Output supply voltage = 3 V		5	10		
		MOC3023			3	5		
V <sub>TM</sub>	Peak on-state voltage, either direction		I <sub>TM</sub> = 100 mA			1.4	3	V
lн	Holding current, either di	rection				100		μΑ

NOTE 5: Test voltage must be applied at a rate no higher than 12  $V/\mu s$ .

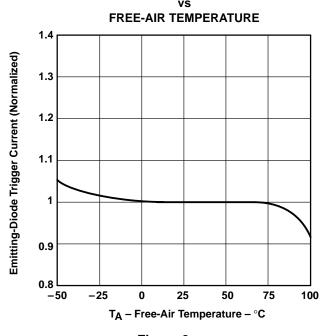
## PARAMETER MEASUREMENT INFORMATION

NOTE A. The critical rate of rise of off-state voltage, dv/dt, is measured with the input at 0 V. The frequency of Vin is increased until the phototriac turns on. This frequency is then used to calculate the dv/dt according to the formula:



#### TYPICAL CHARACTERISTICS

#### **EMITTING-DIODE TRIGGER CURRENT (NORMALIZED)**



# ON-STATE CHARACTERISTICS

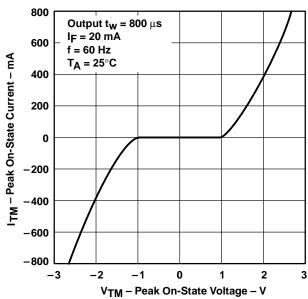


Figure 2

Figure 3

#### **NONREPETITIVE PEAK ON-STATE CURRENT**

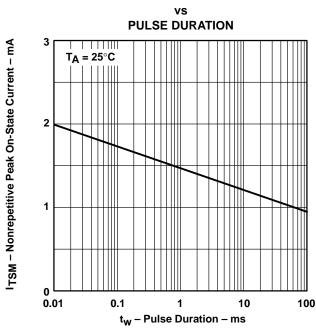


Figure 4

#### **APPLICATIONS INFORMATION**

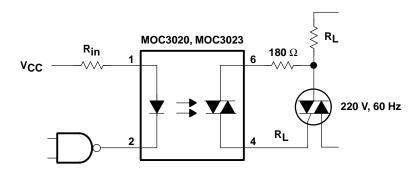


Figure 5. Resistive Load

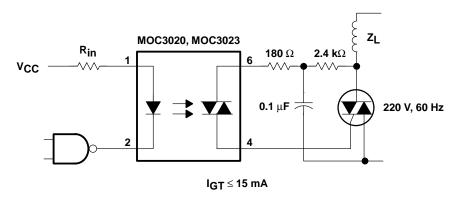


Figure 6. Inductive Load With Sensitive-Gate Triac

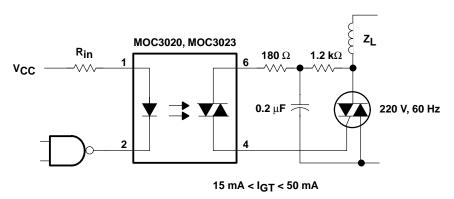
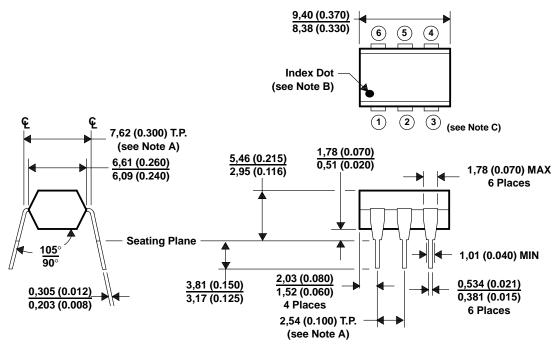


Figure 7. Inductive Load With Nonsensitive-Gate Triac

#### **MECHANICAL INFORMATION**

Each device consists of a gallium-arsenide infrared-emitting diode optically coupled to a silicon phototriac mounted on a 6-terminal lead frame encapsulated within an electrically nonconductive plastic compound. The case can withstand soldering temperature with no deformation and device performance characteristics remain stable when operated in high-humidity conditions.



NOTES: A. Leads are within 0,13 (0.005) radius of true position (T.P.) with maximum material condition and unit installed.

- B. Pin 1 identified by index dot.
- C. Terminal connections:
  - 1. Anode (part of the infrared-emitting diode)
  - 2. Cathode (part of the infrared-emitting diode)
  - 3. No internal connection
  - 4. Main terminal (part of the phototransistor)
  - 5. Triac Substrate (DO NOT connect) (part of the phototransistor)
  - 6. Main terminal (part of the phototransistor)
- D. The dimensions given fall within JEDEC MO-001 AM dimensions.
- E. All linear dimensions are given in millimeters and parenthetically given in inches.

Figure 8. Mechanical Information

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